Title: SIGNAL RECEPTION APPARATUS, SYSTEMS, AND METHODS

Assignee: Intel Corporation

IN THE CLAIMS

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Please amend the claims as follows:

(Currently Amended) A method, including:
 converting a combined plurality P of asynchronous data streams received at substantially
 the same time from a first time domain to a frequency domain;

separating, in a frequency domain, a the combined plurality P of asynchronous data streams received at substantially a same time into a separated plurality of data streams in the frequency domain; and

synchronizing at least one of the separated plurality of data streams in a second time domain.

- 2. (Currently Amended) The method of claim 1, further including:

 converting the combined plurality P of asynchronous data streams from a time domain into the frequency domain prior to the separating the combined plurality P of asynchronous data streams using a channel matrix.
- 3. (Original) The method of claim 1, further including: receiving, at substantially the same time, the combined plurality P of asynchronous data streams at a plurality Q of antennas.
- 4. (Original) The method of claim 1, further including:
 separating the combined plurality P of asynchronous data streams into the separated
 plurality of data streams in the frequency domain using a frequency spatial demapper.
- 5. (Original) The method of claim 1, wherein the separated plurality of data streams correspond directly to a number of wireless channels.

PRELIMINARY AMENDMENT

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6. (Original) The method of claim 1, wherein at least one of the separated plurality of data streams is formatted according to one of an Institute of Electrical and Electronics Engineers 802.11 standard and an Institute of Electrical and Electronics Engineers 802.16 standard.

- 7. (Currently Amended) The method of claim 1, further including:

 converting the separated plurality of data streams into the second time domain prior to the synchronizing at least one of the separated plurality of data streams in the time domain.
- 8. (Currently Amended) An article including a machine-accessible medium having associated information, wherein the information, when accessed, results in a machine performing:

converting a combined plurality P of asynchronous data streams received at substantially the same time from a first time domain to a frequency domain;

separating the combined plurality P of asynchronous data streams into a separated plurality of data streams in the frequency domain; and

synchronizing at least one of the separated plurality of data streams in a second time domain. separating, in a frequency domain, a combined plurality P of asynchronous data streams received at substantially a same time into a separated plurality of data streams.

- 9. (Original) The article of claim 8, wherein the separating is performed by a wireless access point.
- 10. (Original) The article of claim 8, wherein the information, when accessed, results in the machine performing:

computing a frequency response for a number of channels corresponding to the plurality P of asynchronous data streams.

11. (Currently Amended) The article of claim 8, wherein the information, when accessed, results in the machine performing:

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converting the separated plurality of data streams in the frequency domain into a separated plurality of data streams in the <u>second</u> time domain.

12. (Original) The article of claim 8, wherein the information, when accessed, results in the machine performing:

synchronizing at least one of the separated plurality of data streams after detecting a presence of a short preamble.

13. (Original) The article of claim 12, wherein the information, when accessed, results in the machine performing:

estimating a coarse frequency offset.

14. (Currently Amended) An apparatus, including:

a module to separate, in a frequency domain, a combined plurality P of asynchronous data streams received at substantially a same time into a separated plurality of data streams, after the combined plurality P of asynchronous data streams have been converted from a first time domain to the frequency domain; and

a synchronization module to synchronize at least one of the separated plurality of data streams in a second time domain.

- 15. (Original) The apparatus of claim 14, wherein the module to separate further includes: a spatial demultiplexer to provide the separated plurality of data streams.
- 16. (Original) The apparatus of claim 14, wherein the module to separate further includes: a module to perform a fast Fourier transform on the combined plurality P of asynchronous data streams; and

a module to perform an inverse fast Fourier transform on at least one of the separated plurality of data streams.

17. (Currently Amended) The apparatus of claim 14, further including:

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a synchronization module wherein the synchronization module is to receive at least one of the separated plurality of data streams after processing by a module capable of performing an inverse fast Fourier transform.

18. (Original) The apparatus of claim 14, wherein at least one of the separated plurality of data streams is formatted according to one of an Institute of Electrical and Electronics Engineers 802.11 standard and an Institute of Electrical and Electronics Engineers 802.16 standard.

19. (Currently Amended) An apparatus, including:

a module to perform a fast Fourier transform on a combined plurality P of asynchronous data streams;

a spatial demultiplexer to provide a separated plurality of data streams associated with the combined plurality P of asynchronous data streams; and

a module to perform an inverse fast Fourier transform on at least one of the separated plurality of data streams so as to separate, in a frequency domain, the combined plurality P of asynchronous data streams received at substantially a same time into the separated plurality of data streams; and

a module to synchronize at least one of the separated plurality of data streams in a time domain.

- 20. (Original) The apparatus of claim 19, wherein at least some of the separated plurality of data streams include a plurality of orthogonal frequency division multiplexed symbols.
- 21. (Original) The apparatus of claim 19, wherein a frequency offset associated with a first data stream included in the separated plurality of data streams is different than a frequency offset associated with a second data stream included in the plurality of separated data streams.
- 22. (Currently Amended) A system, including:

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a module to separate, in a frequency domain, a combined plurality P of asynchronous data streams received at substantially a same time into a separated plurality of data streams[[;]], after the combined plurality P of asynchronous data streams have been converted from a first time domain to the frequency domain;

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a synchronization module to synchronize at least one of the separated plurality of data streams in a second time domain; and

a plurality Q of antennas to receive the combined plurality P of asynchronous data streams.

- 23. (Original) The system of claim 22, wherein the plurality Q of antennas form a portion of a multiple-input, multiple-output (MIMO) system.
- 24. (Original) The system of claim 22, further including: a wireless access point coupled to the plurality Q of antennas.
- 25. (Original) The system of claim 24, wherein the wireless access point is to train at least one channel for at least some of a plurality of P users associated with the combined plurality P of asynchronous data streams.
- 26. (Original) The system of claim 22, further including: a processor to form a Q x P channel matrix.